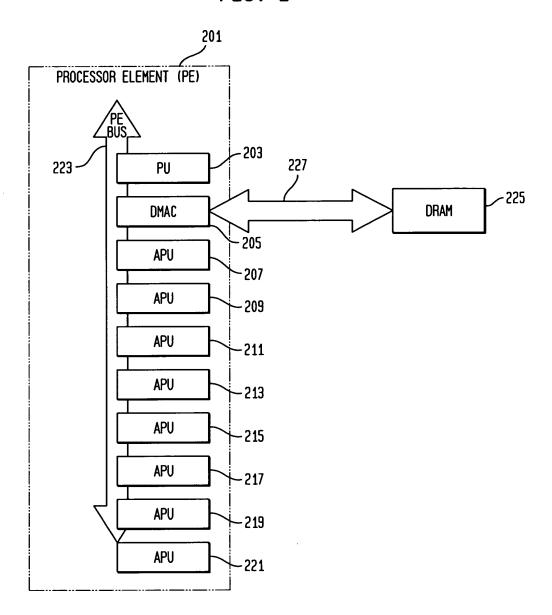




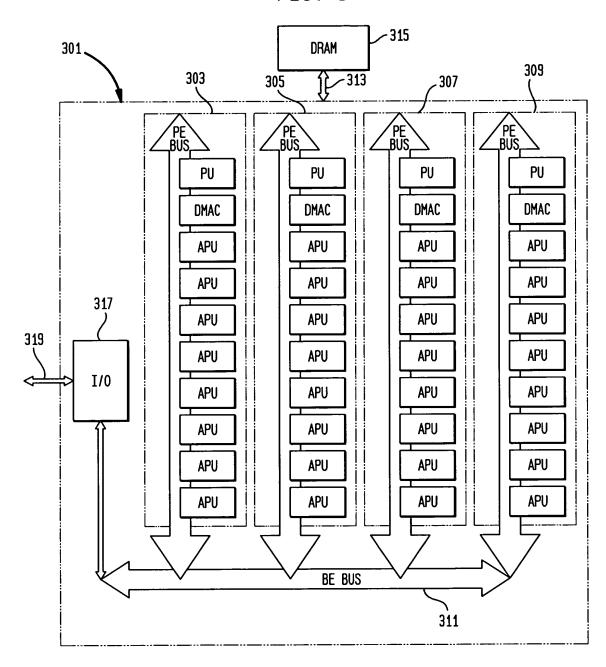
2/42 FIG. 2



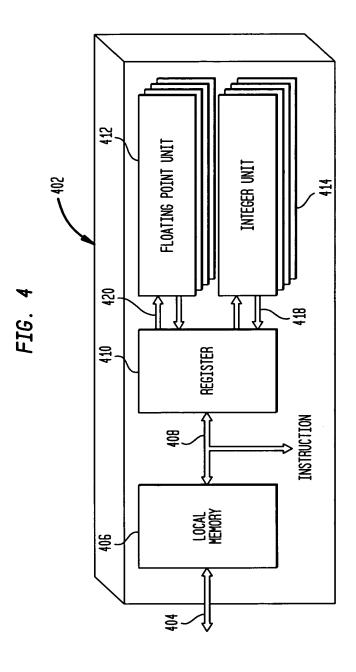


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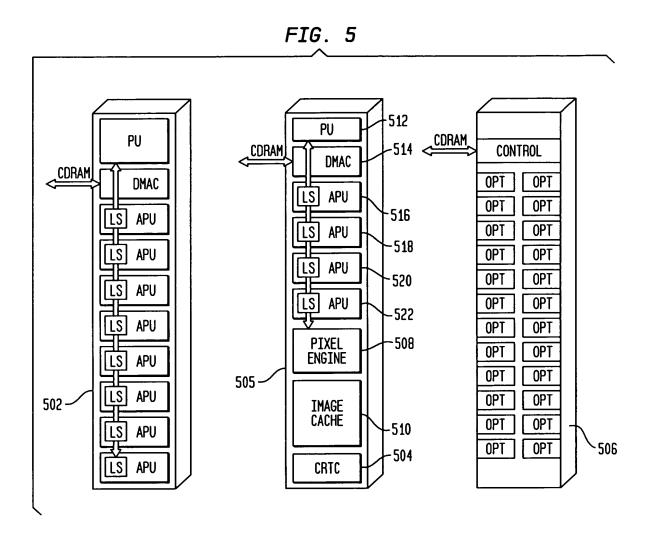
FIG. 3



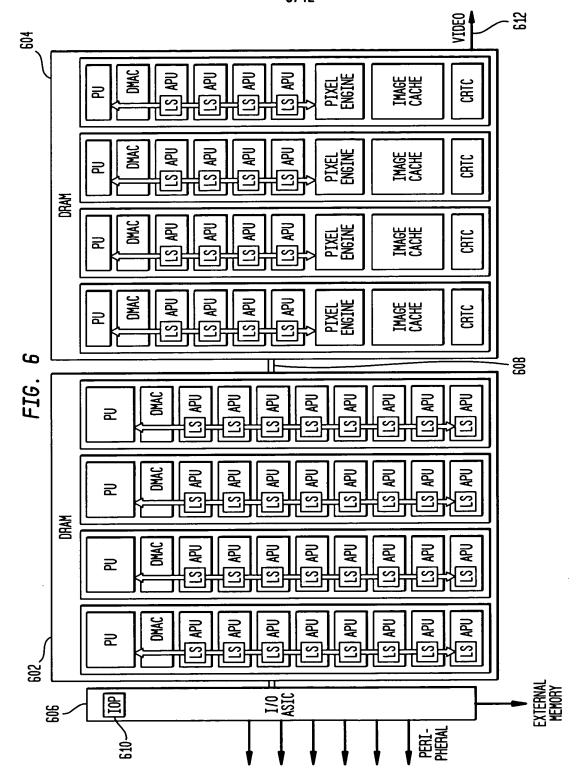






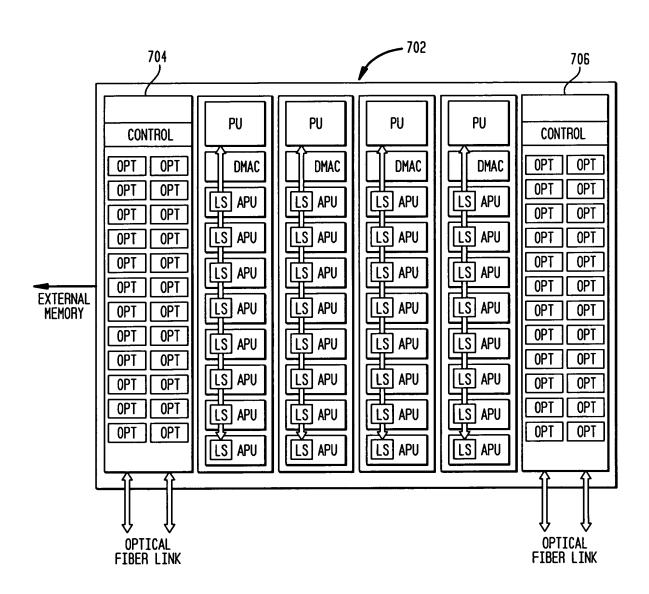




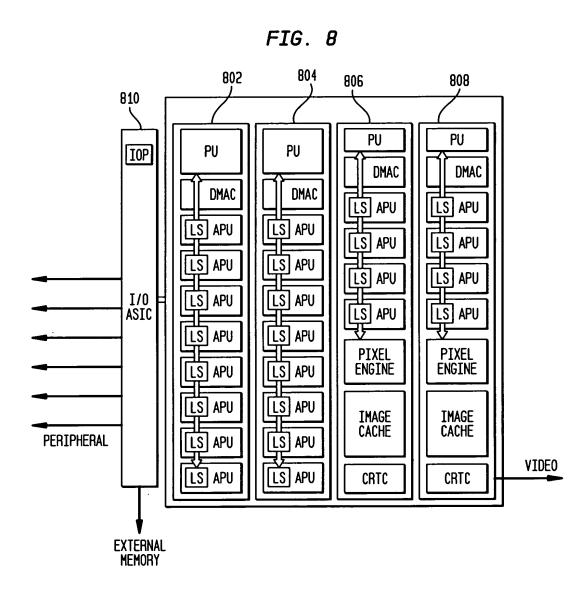




7/42 FIG. 7









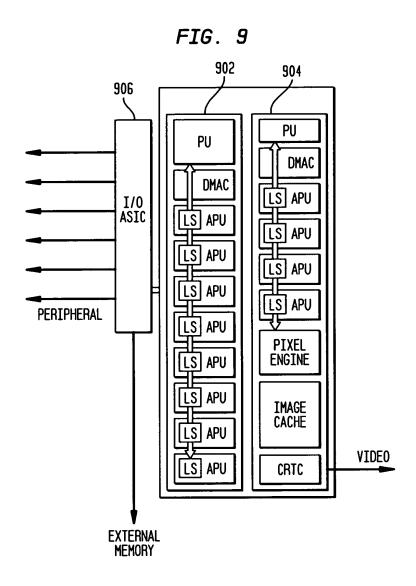
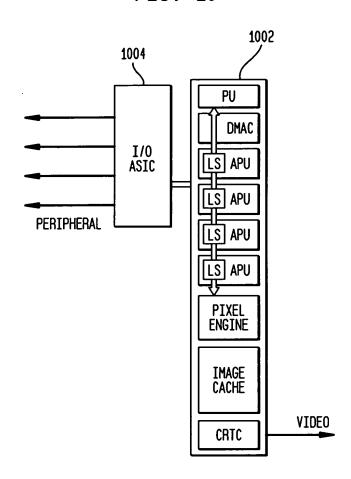
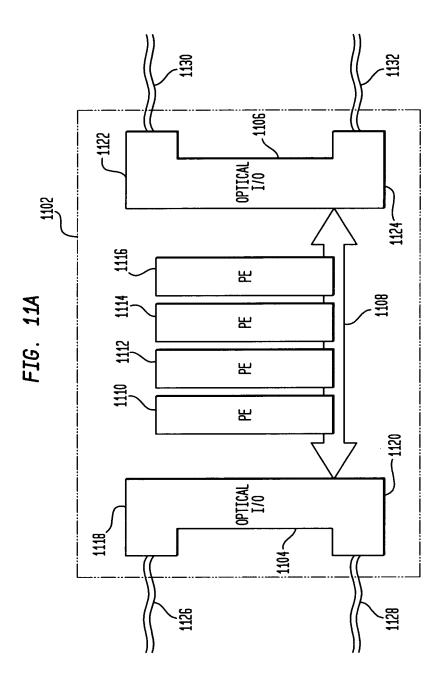




FIG. 10









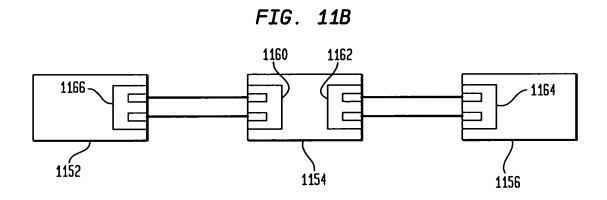
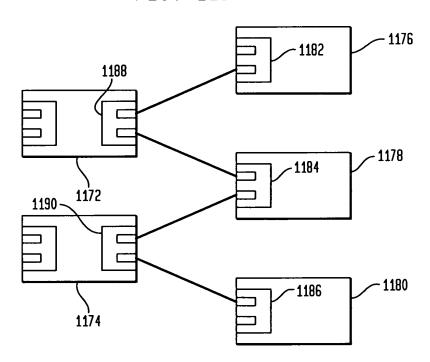
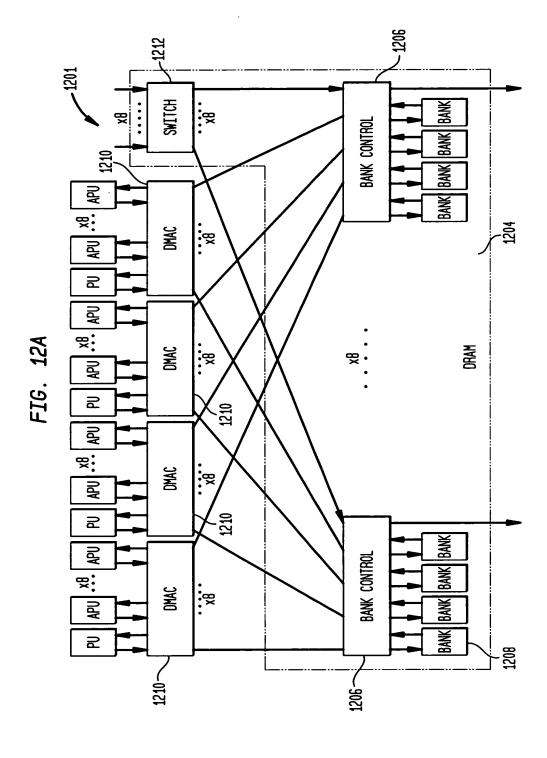


FIG. 11C









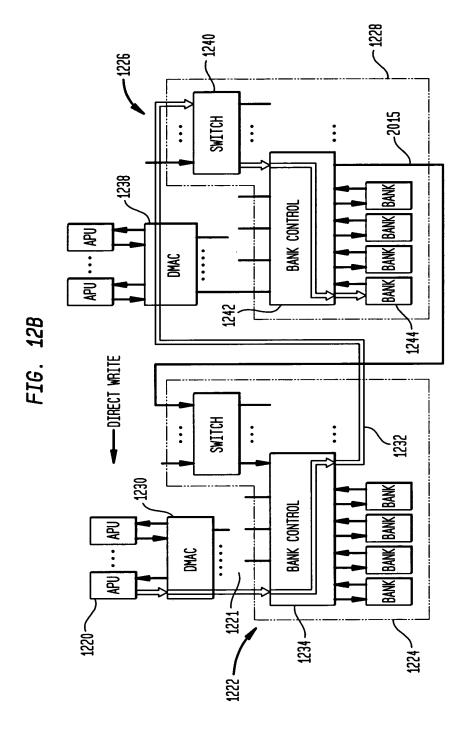
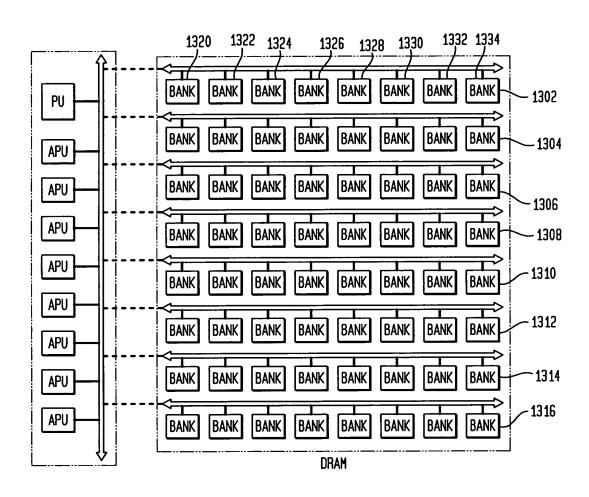




FIG. 13





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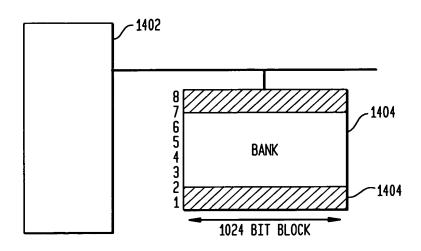
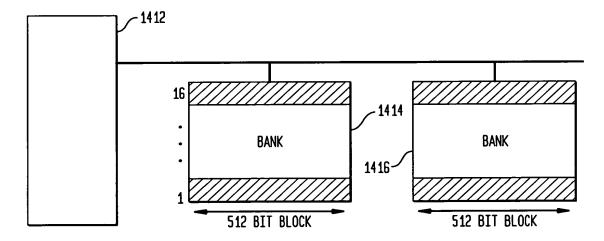
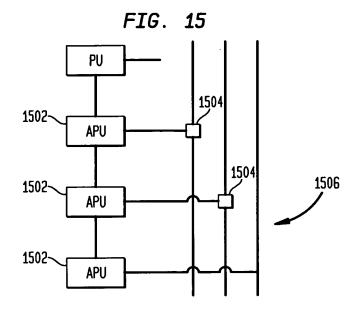
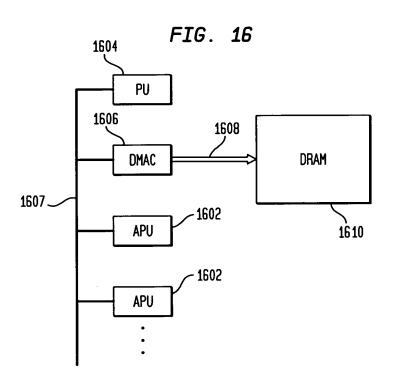


FIG. 14B











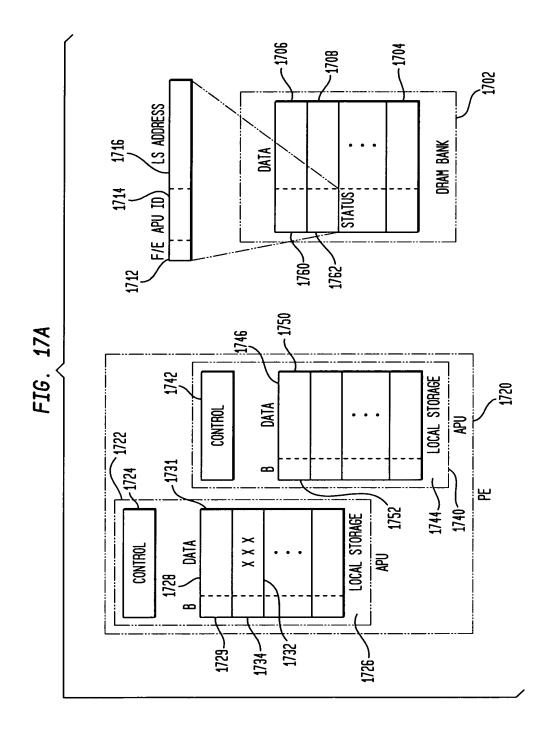
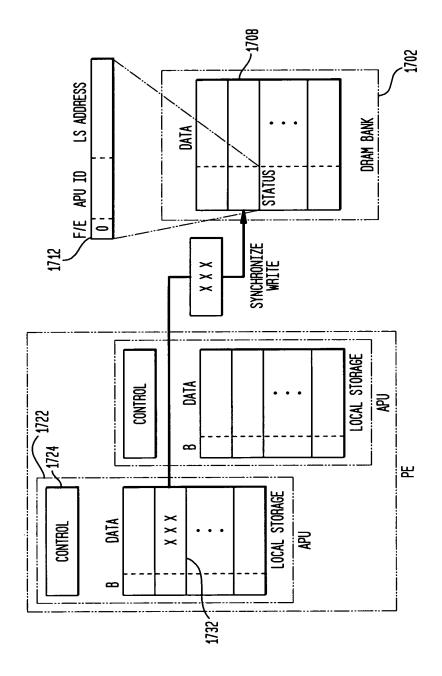
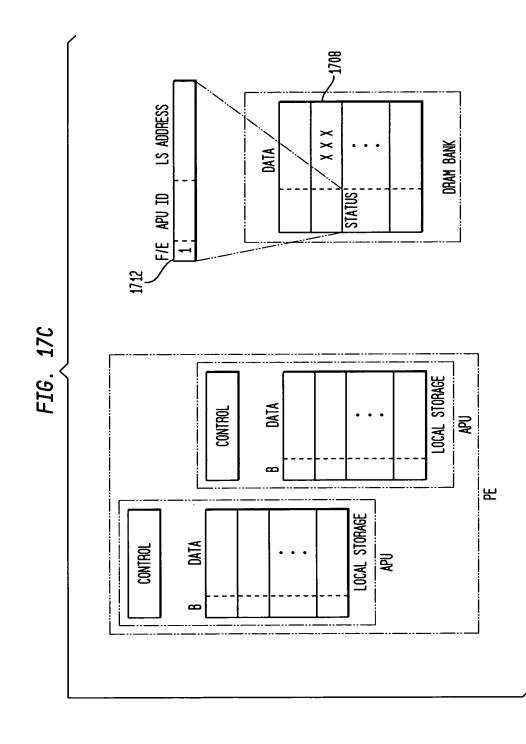




FIG. 17B









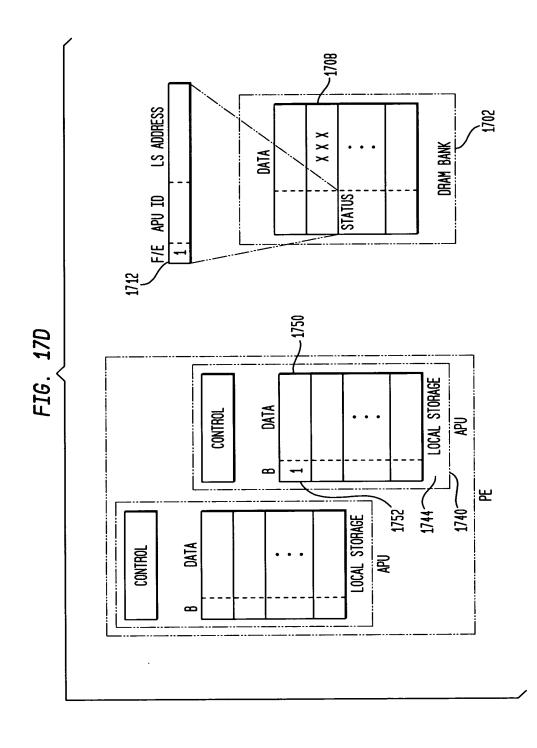
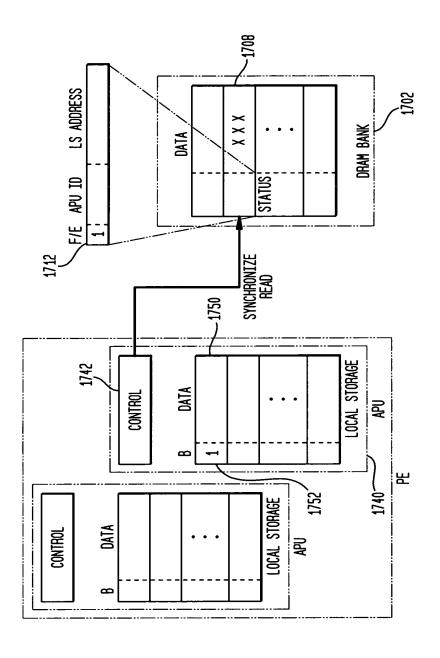
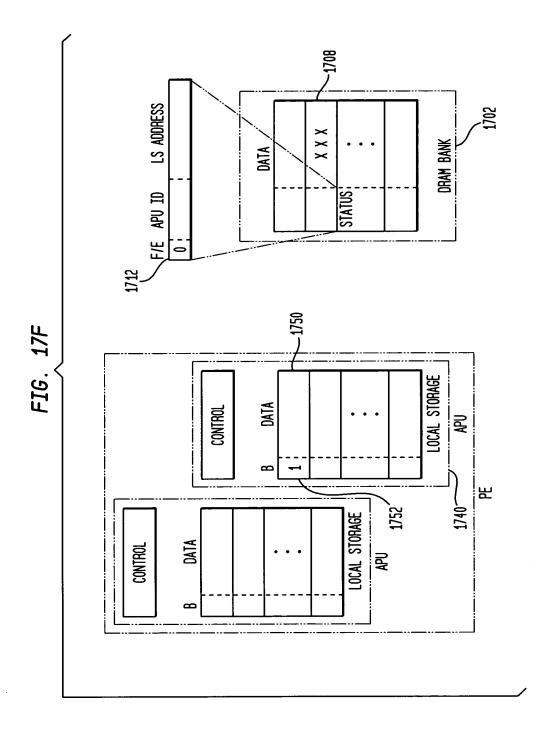




FIG. 17E





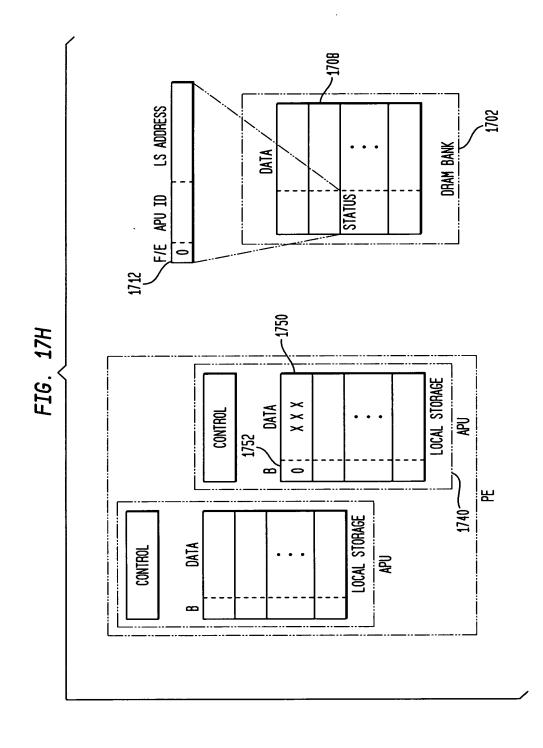




LS ADDRESS DRAM BANK DATA F/E APU ID READ DATA LOCAL STORAGE DATA CONTROL ₽ 出 LOCAL STORAGE DATA CONTROL Æ 8

FIG. 176







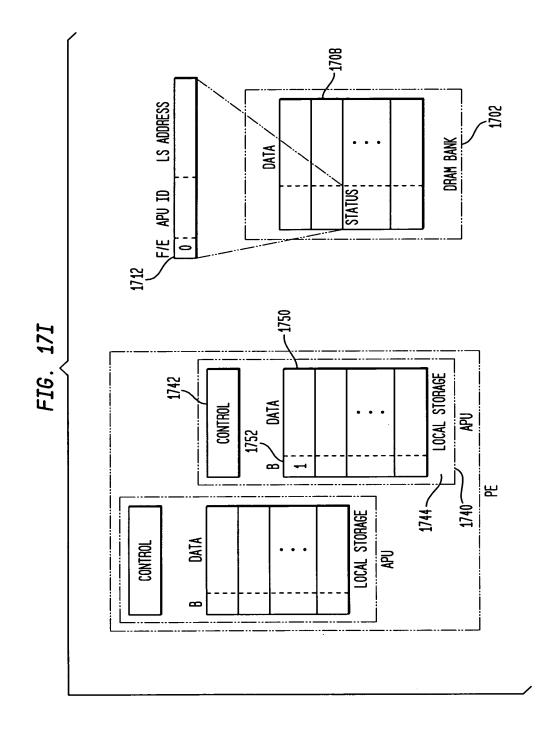
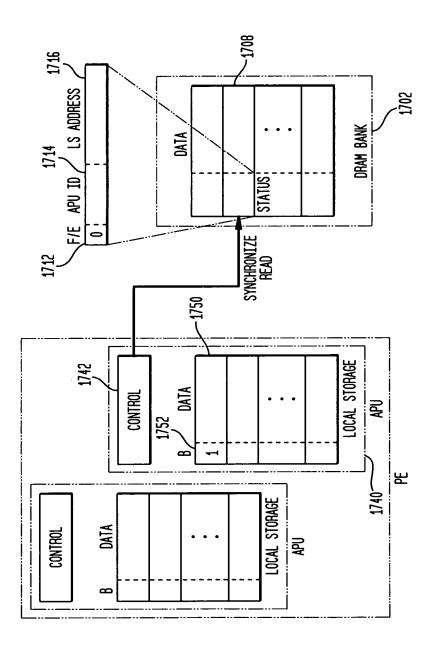




FIG. 17J





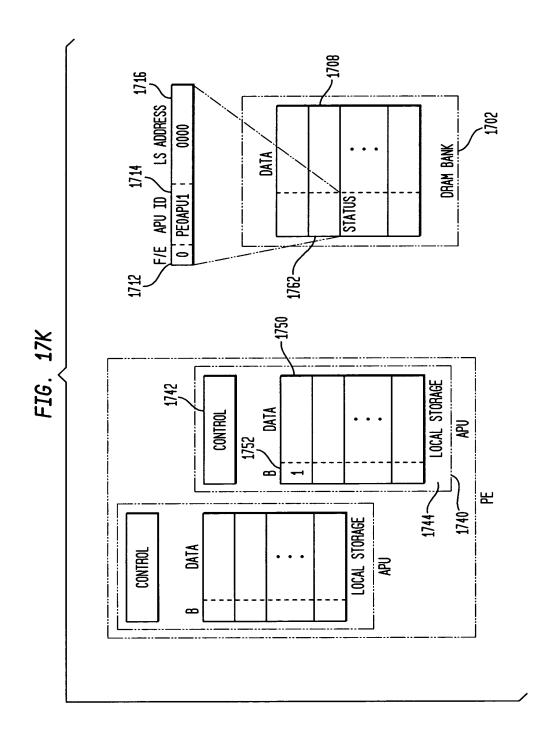
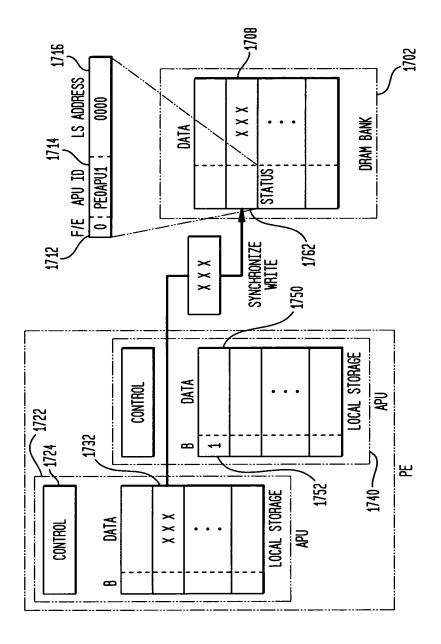
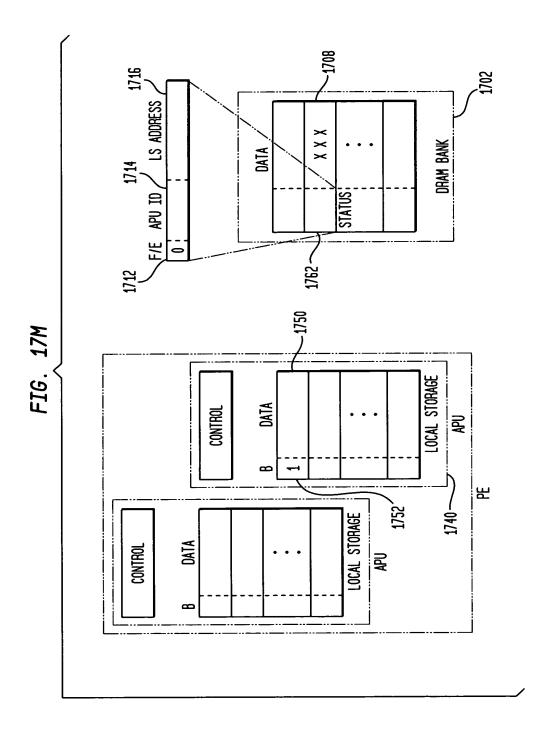




FIG. 17L



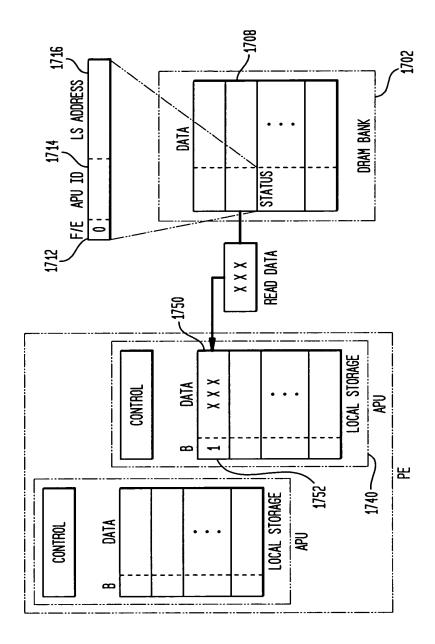




.



FIG. 17N





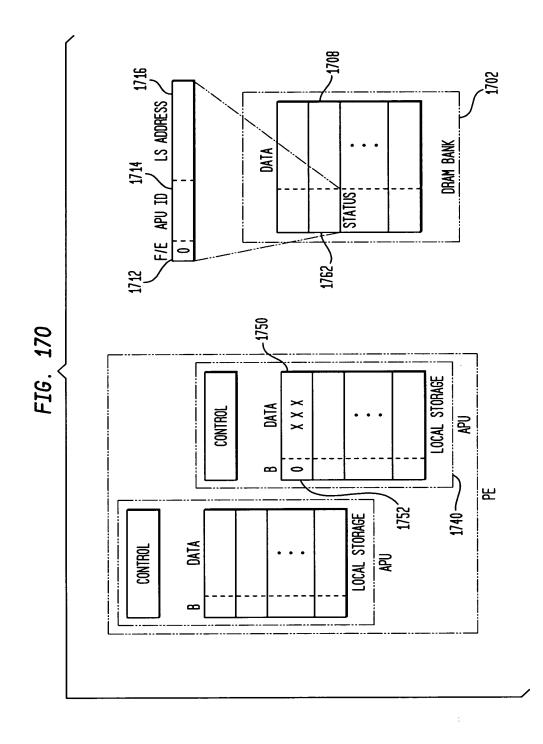




FIG. 18

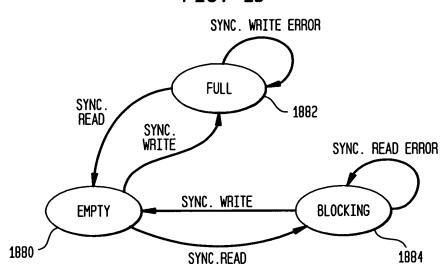


FIG. 19

1904 1906 1908 1902 O					
1 APU KEY KEY MASK 2 APU KEY KEY MASK	1904	_ID	1906	:	1908 1902
2 APU KEY KEY MASK .		0	APU KEY	KEY MASK	
•		1	apu key	KEY MASK	
7 APU KEY KEY MASK		2	APU KEY	KEY MASK	
7 APU KEY KEY MASK			•		
7 APU KEY KEY MASK			•	,	
7 APU KEY KEY MASK			•	·	
		7	APU KEY	KEY MASK	



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FIG. 20

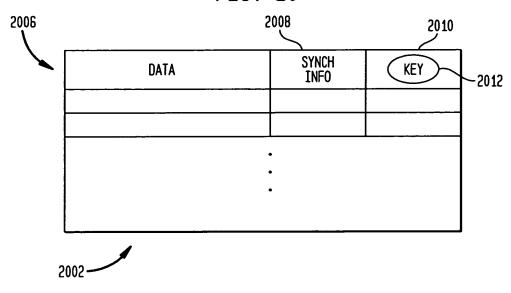


FIG. 21

ID	2106		2108 2	110 2117
0	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK
1	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK
2	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK
		•		
		•		
		•		
63	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK
7		•		
,				2102



FIG. 22

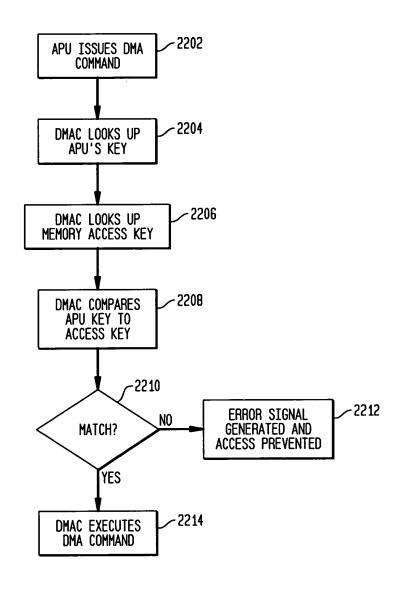
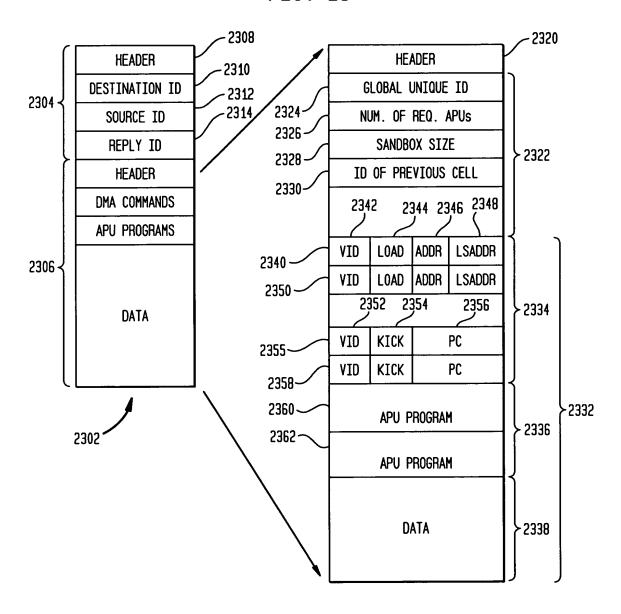
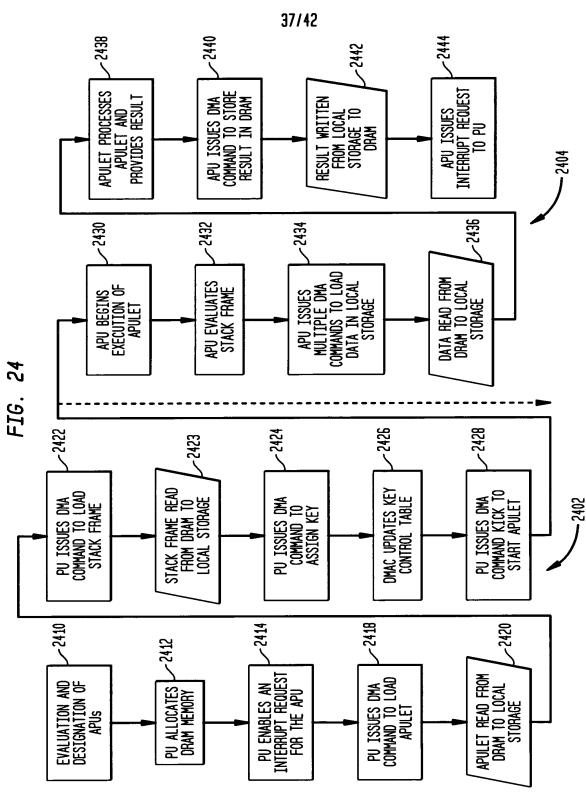




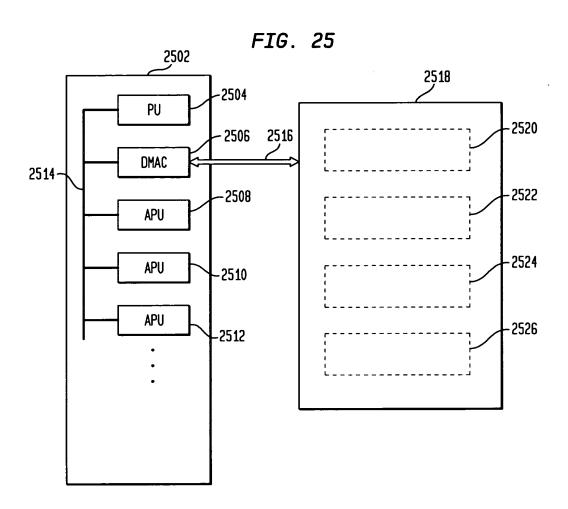
FIG. 23



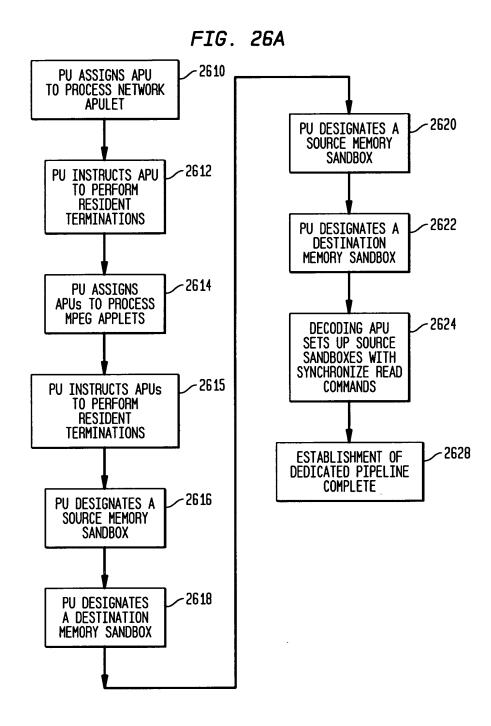














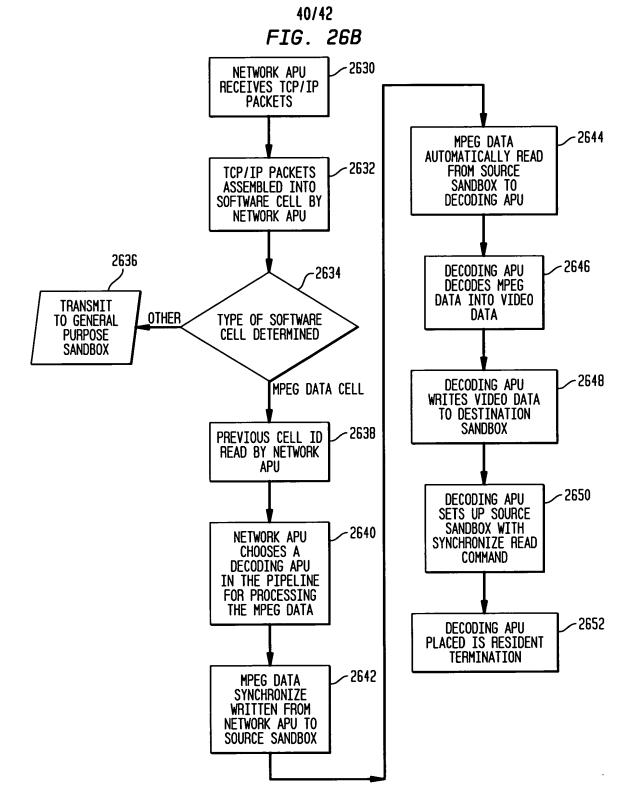




FIG. 27

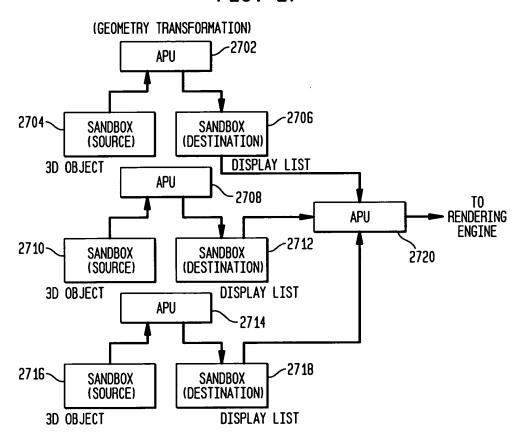




FIG. 28

